

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application Of:

Examiner: Unknown

Ejaz Ul Haq

Art Unit: Unknown

Serial No: Unknown

Filed: Herewith

For: High Speed Synchronous Signaling
For Interfacing VLSI CMOS Circuits
To Transmission Lines

BOX PATENT APPLICATION

Commissioner of Patents
Washington, D.C. 20231

GENERAL AUTHORIZATION TO PETITION FOR EXTENSIONS OF TIME

Dear Sir:

With reference to the subject application, and pursuant to 37 C.F.R. § 1.136, Applicants hereby authorize and request the Commissioner to treat any correspondence requiring a petition for extension of time as containing such a request therefor for the appropriate length of time. This general authorization is effective during the pendency of this application, including any division or continuing application therefrom.

Where no check is received by the Commissioner, you are hereby authorized to charge payment of the requisite petition fees, or charge any additional fee required under 37 C.F.R. § 1.17, or credit any overpayment of same, to Deposit Account No. 05-0150.

Date: May 8, 2001
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Respectfully submitted,

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